

### **Amendments to the Claims:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

### **Listing of Claims:**

Claim 1 (currently amended): A system comprising:  
a process comprising multiple-mapped memory;  
a first set of memory mapped onto the multiple-mapped memory[[]];  
a second set of memory mapped onto the multiple-mapped memory; and  
an address overload circuit to selectively map the multiple-mapped memory to the first set of memory or to the second set of memory, the address overload circuit comprising an address multiplexer, an address translator coupled to the address multiplexer, and a data multiplexer.

Claim 2 (currently amended): A system as defined in Claim 1, wherein the second set of memory comprises instructions ~~that are effective~~ to execute a protected function.

Claim 3 (currently amended): A system as defined in Claim [[2]] 1, further comprising a transfer agent to receive parameters from the process and to assume control of execution of the process when the multiple-mapped memory is mapped to a protected set of memory.

Claim 4 (currently amended): A system as defined in Claim 3, wherein the transfer agent is ~~effective~~ to call a protected function.

Claim 5 (currently amended): A system as defined in Claim 4, wherein the transfer agent is ~~effective~~ to call the protected function using parameters received from the process.

Claim 6 (original): A system as defined in Claim 4, wherein the transfer agent is stored on nonvolatile memory.

Claim 7 (currently amended): A system as defined in Claim 6, wherein the transfer agent ~~executes~~ is to execute on internal memory.

Claim 8 (cancel)

Claim 9 (currently amended): A method comprising:

executing a process that comprises multiple-mapped memory;  
determining whether the process is a trusted process;  
if the process is determined not to be a trusted process, mapping the multiple-mapped memory to unprotected memory; and  
if the process is determined to be a trusted process, mapping the multiple-mapped memory to protected memory, copying a transfer agent to a second memory, transferring parameters from the process to the transfer agent, and controlling execution of the process with the transfer agent.

Claim 10 (cancel)

Claim 11 (currently amended): A method as defined in Claim [[10]] 9, wherein the transfer agent is stored in a first memory.

Claim 12 (cancel)

Claim 13 (currently amended): A method as defined in Claim [[12]] 9, further comprising:

executing the transfer agent so as to identify a protected function and to call the protected function.

Claim 14 (original): A method as defined in Claim 13, further comprising:

executing the protected function.

Claim 15 (currently amended): A method as defined in Claim [[12]] 9, further comprising:

operating a trust co-processor to determine whether the process is a trusted process.

Claim 16 (original): A method as defined in Claim 15, further comprising:

executing the transfer agent so as to identify a protected function and to call the protected function.

Claim 17 (original): A method as defined in Claim 16, further comprising:

executing the protected function.

Claim 18 (currently amended): An article comprising a machine-readable storage medium on which there are stored instructions that, if executed, enable a system to:

determine whether a process is a trusted process; ~~and~~

if the process is a trusted process, transfer, at least temporarily, control of the process to a transfer agent and transfer process parameters to the transfer agent;

identify and execute a protected function; and

copy the transfer agent from nonvolatile memory to volatile memory in the course of executing multiple-mapped memory.

Claims 19-21 (cancel)

Claim 22 (currently amended): An article as defined in Claim 18, wherein the instructions, if executed, enable the system to determine whether ~~[[a]]~~ the process is a trusted process in response to the detection of the multiple-mapped memory.

Claim 23 (cancel)

Claim 24 (currently amended): An article as defined in Claim ~~[[23]]~~ 18, wherein the instructions, if executed, enable the system to:

by operation of the transfer agent, identify, call, and execute ~~a protected~~ the trusted process.

Claim 25 (cancel)

Claim 26 (currently amended): A system comprising:  
an integrated circuit device comprising a processor, internal random access memory (RAM), and internal read only memory (ROM);

unprotected memory;

protected memory;

a process to execute on the internal RAM, the process comprising multiple-mapped memory, the multiple-mapped memory to be ~~selectively~~ mapped to either the protected memory or the unprotected memory;

a trust co-processor to determine whether the multiple-mapped memory is to be mapped to the unprotected memory or ~~is to be mapped~~ to the protected memory;

a circuit coupled to the trust co-processor to map the multiple-mapped memory to the protected memory, the circuit comprising an address multiplexer, an address translator coupled to the address multiplexer, and a data multiplexer;

a wireless interface coupled to the processor; and

an antenna coupled to the wireless interface.

Claims 27 – 28 (cancel)

Claim 29 (original): A system as defined in Claim 26, further comprising a transfer agent to receive parameters from a trusted process, call a protected function using the parameters, and cause the protected function to execute.

Claim 30 (cancel)

Claim 31 (new): The system of Claim 26, further comprising a memory controller including the circuit.

Claim 32 (new): An apparatus comprising:

an address selector to receive an address and a translated address, the address selector to provide an output mapped to a respective one of a protected storage and an unprotected storage; and

a data selector to receive data from the protected storage and the unprotected storage.

Claim 33 (new): The apparatus of Claim 32, further comprising an address translator coupled to the address selector to generate the translated address.

Claim 34 (new): The apparatus of Claim 33, wherein the address selector is to map the output to the protected storage if a process is trusted.

Claim 35 (new): The apparatus of Claim 34, further comprising a trust coprocessor to determine if the process is trusted.

Claim 36 (new): The apparatus of Claim 35, wherein the address selector and the data selector are to be controlled by a control signal from the trust coprocessor.